



#7/Response
(HE)
Vbromy
5/3/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Chuen-Der Lien and Chau Chin Wu
Assignee: Integrated Device Technology, Inc.
Title: "LOW-POWER CONTENT ADDRESSABLE MEMORY CELL"
Serial No.: 09/185,057 Filed: November 2, 1998
Examiner: H. Ho Group Art Unit: 2818
Attorney Docket No.: IDT-1548

San Jose, CA
April 28, 2000

BOX AF
Assistant Commissioner for Patents
Washington, D.C. 20231

RESPONSE TO FINAL OFFICE ACTION

Sir:

Applicants submit the following remarks in response to the Final Office Action mailed on March 3, 2000.

REMARKS

Claim 20 has been rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent No. 5,351,208, issued Sep. 27, 1994 to Jiang (hereinafter "Jiang"). Applicant respectfully traverses this rejection.

Claim 20 recites a CAM cell "having a match line ... having a signal swing equal to one transistor threshold voltage." (Emphasis added.) Thus, the difference (swing) between the voltage on the match line during a match condition and the voltage on the match line during a no-match condition is equal to one transistor threshold voltage. (Specification, page 14, line 25 to page 15, line 14.)

Referring to Fig. 2 of Jiang, the Examiner indicates that "the match line 214 has a signal swing equal to one transistor threshold voltage of the match transistor 213". (Final Office